

FORM PTO-1449

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APPLICANT

Korbin S. Van Dyke, et al.

FILING DATE

June 16, 1999

GROUP AND UNIT

2784

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
C.D.	2,969,969	12/01/1970	Rakoczi	340	172.5	Nov. 27, 1967
C.D.	3,781,823	12/25/1973	Senese	340	172.5	July 28, 1972
C.D.	4,084,235	04/11/1978	Hirtle	364	200	Apr. 14, 1975
C.D.	4,455,602	06/19/1984	Baxter	364	200	May 22, 1981
C.D.	4,575,797	03/11/1986	Gruner	364	200	May 22, 1981
C.D.	5,542,109	07/30/1996	Blomgren	395	800	Aug. 31, 1994
C.D.	5,638,525	06/10/1997	Hammond	395	385	Feb. 10, 1995
C.D.	5,659,679	08/19/1997	Alpert	395	183.1	May 30, 1995
C.D.	5,796,939	08/18/1998	Berc, et al.	395	184.01	Mar. 10, 1997
C.D.	5,802,272	09/01/1998	Sites, et al.	395	183.21	Dec. 19, 1994
C.D.	5,802,373	09/01/1998	Yates	395	705	Jan. 29, 1996
C.D.	5,822,578	10/13/1998	Frank	395	591	June 5, 1995
C.D.	5,832,205	11/03/1998	Kelly	395	185.06	Aug. 20, 1996
C.D.	5,930,509	07/27/1999	Yates	395	707	Jan. 29, 1996
C.D.	5,949,415	09/07/1999	Lin	345	335	Jun. 16, 1997
C.D.	5,958,047	09/28/1999	Panwar	712	237	Jun. 25, 1997
C.D.	6,000,028	12/07/1999	Chernoff	712	226	Jan. 29, 1996
C.D.	6,091,897	07/18/2000	Yates	395	708	Jan. 29, 1996
C.D.						

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
C.D.	WO 96/24895 Hammond	08/15/1996	PCT/US	G06F	9/22		

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Papers, Etc.)

C.D.	Burroughs, Preliminary Edition, B 1700 Systems Reference Manual, Burroughs Corporation (1972)
C.D.	Burroughs, B 1700 Systems Micro Implementation Language (MIL) Reference Manual, Burroughs Corporation (1973)
C.D.	Anton Chernoff, et al., FX!32, A Profile-Directed Binary Translator, IEEE Micro, Vol. 18 No. 2 pp. 56-64 (March/April, 1998)
C.D.	T.M. Conte, et al., "Accurate and practical profile-driven compilation using the profile buffer," Proceedings of the 29 th Annual International Symposium on Microarchitecture, Paris, France, pp. 36-45 (Dec. 1996)

EXAMINER

Chameli C. Das

DATE CONSIDERED

12/22/02

FORM PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 30585/10	SERIAL NO. 09/334,530
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)				APPLICANT Korbin S. Van Dyke, et al.	
				FILING DATE June 16, 1999	GROUP ART UNIT 2784
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Papers, Etc.)					
C.D			T.M. Conte, et al., "Hardware based profiling: An effective technique for profile-driven optimization," International Journal of Parallel Programming, vol. 24, no. 2 (Feb. 1996)		
C.D			T.M. Conte, et al., "Using branch handling hardware to support profile-driven optimization," Proceedings of the 27 th Annual International Symposium on Microarchitecture San Jose, CA (Dec. 1994)		
C.D			J.S. Cox et al., "Commercializing profile-driven optimization." Proceedings of the 28 th Hawaii International Conference on System Sciences, vol. 1 Maui, HI, pp. 221-228 (Jan. 1995)		
C.D			Digital Equipment Corp., White Paper: How DIGITAL FX!32 Works (September 1997) http://www.digital.com/semiconductor/amt/fx32/fx-white.htm		
C.D			Ebcioglu and Altman, IBM Research Report, DAISY: Dynamic Compilation for 100% Architectural Compatibility, IBM Research Division (1996) ✓		
C.D			Linda Geppert et al., Transmeta's Magic Show, IEEE Spectrum, vol. 37 no. 5, pp. 26-33 (May 2000)		
C.D			Ronald M. Guffin, Microdiagnostics for the Standard Computer MLP-900 Processor, IEEE Transactions on Computers, Vol. C-20 No. 7, pp. 803-808 (July 1971)		
C.D			Linley Gwennap, MDR Technical Library Special Report, Intel's Merced and IA-64 Technology and Market Forecast, IA-64 Software Model - Chapter 3 excerpt Microprocessor Report, MicroDesign Resources (July 20, 1998)		
C.D			Linley Gwennap, MDR Technical Library Special Report, Intel's Merced and IA-64: Technology and Market Forecast, Executive Summary, Microprocessor Report, MicroDesign Resources (July 20, 1998)		
C.D			Linley Gwennap, First Merced Patent Surfaces, Intel Document Reveals Processors With Dual Instruction Sets, Microprocessor Report, MicroDesign Resources (July 20, 1998)		
C.D			R.E. Hank, et al., Proceeding of the 28 th Annual International Symposium on Microarchitecture, pp. 158-168 (Dec. 1995)		
C.D			Raymond J. Hookway, et al., Combining Emulation and Binary Translation (August 28, 1997) http://www.digital.com/info/DTJP01HM.HTM		
C.D			Harold W. Lawson, Jr., et al., Functional Characteristics of a Multilingual Processor, IEEE Transactions on Computers, Vol. C-20, No. 7, pp. 732-742 (July, 1971)		
C.D			K.N.P. Menezes, "Hardware-based profiling for program optimization," Ph.D. thesis, Department of Electrical and Computer Engineering, North Carolina State University, Raleigh, North Carolina (1997)		
C.D			Nanodata Corporation, QM-1 Hardware level User's Manual (1975)		
C.D			Nanodata Corporation, QM-2 Processor (1976)		
EXAMINER Chambl. C. Da			DATE CONSIDERED 12/27/02		

